

Appl. No.:

10/790,764

Confirmation No. 5129

Applicant:

N. TAKAURA et al.

Filed:

March 3, 2004

Title:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

TC/AU:

2818

Examiner:

T.T.V. Ho

Customer No.: 24956

## AMENDMENT UNDER 37 C.F.R. § 1.312(a)

MAIL STOP: ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

After allowance, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.